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APPLICANTS

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** CONTINUING DATA *****

This application is a CON of PCT/EP02/09702 08/30/2002

** FOREIGN APPLICATIONS *****

GERMANY 101 51 202.3 10/17/2001

IF REQUIRED, FOREIGN FILING LICENSE GRANTED **

06/26/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY GERMANY	SHEETS DRAWING 3	TOTAL CLAIMS 2019	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <u>[Signature]</u> Examiner's Signature <u>[Initials]</u> Initials				

ADDRESS

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TITLE

Semiconductor structure having a compensated resistance in the LDD area and method for producing the same

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